

**Digital Logic IV:  
Finite State Machines**


CMSC 313  
Sections 01, 02

**Sequential Circuits**

2

**TOPICS TODAY**

- Finite State Machines
- Example: Mod-4 Counter
- Example: Vending Machine



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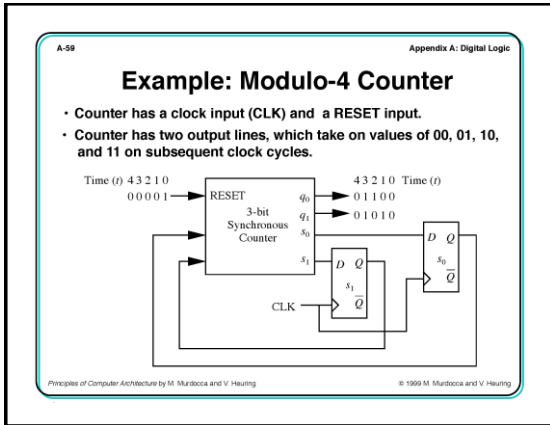
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# FINITE STATE MACHINES

# EXAMPLE: MOD 4 COUNTER



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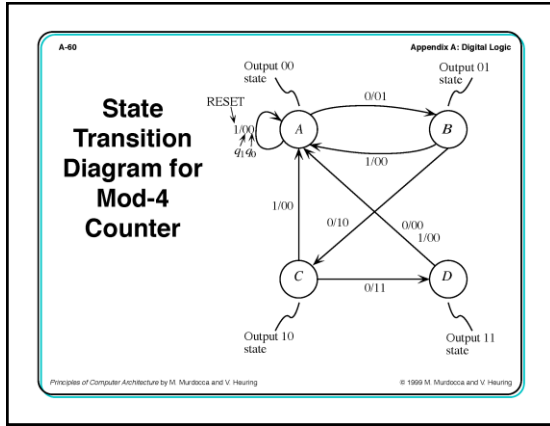
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**State Table for Mod-4 Counter**

Present state	Input	RESET	
		0	1
A		B/01	A/00
B		C/10	A/00
C		D/11	A/00
D		A/00	A/00

Next state      Output

Appendix A: Digital Logic

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**State Assignment for Mod-4 Counter**

Present state ( $S_i$ )	Input	RESET	
		0	1
A:00		01/01	00/00
B:01		10/10	00/00
C:10		11/11	00/00
D:11		00/00	00/00

Appendix A: Digital Logic

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A-63 Appendix A: Digital Logic

### Truth Table for Mod-4 Counter

RESET $r(t)$	$s_1(t)$	$s_0(t)$	$s_1 s_0(t+1)$	$q_1 q_0(t+1)$
0	0	0	01	01
0	0	1	10	10
0	1	0	11	11
0	1	1	00	00
1	0	0	00	00
1	0	1	00	00
1	1	0	00	00
1	1	1	00	00

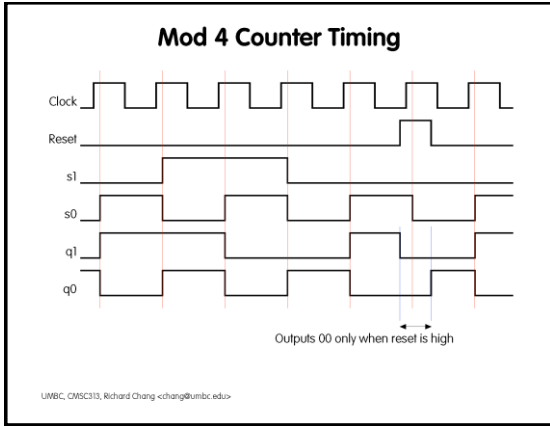
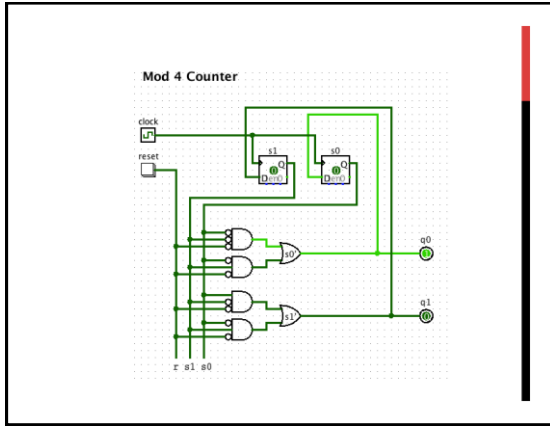
$$s_0(t+1) = \overline{r(t)}\overline{s_1(t)}\overline{s_0(t)} + \overline{r(t)}s_1(t)\overline{s_0(t)}$$

$$s_1(t+1) = \overline{r(t)}\overline{s_1(t)}s_0(t) + \overline{r(t)}s_1(t)s_0(t)$$

$$q_0(t+1) = \overline{r(t)}\overline{s_1(t)}\overline{s_0(t)} + \overline{r(t)}s_1(t)\overline{s_0(t)}$$

$$q_1(t+1) = \overline{r(t)}\overline{s_1(t)}s_0(t) + \overline{r(t)}s_1(t)s_0(t)$$

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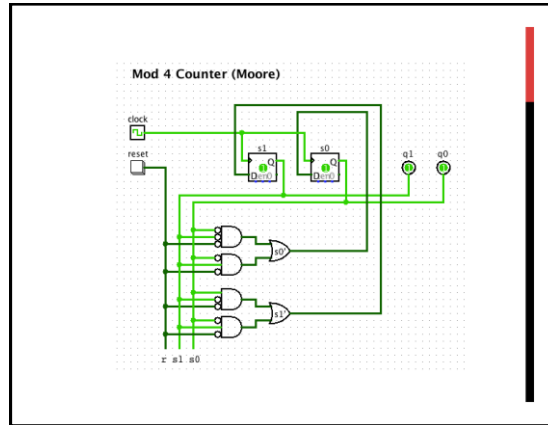
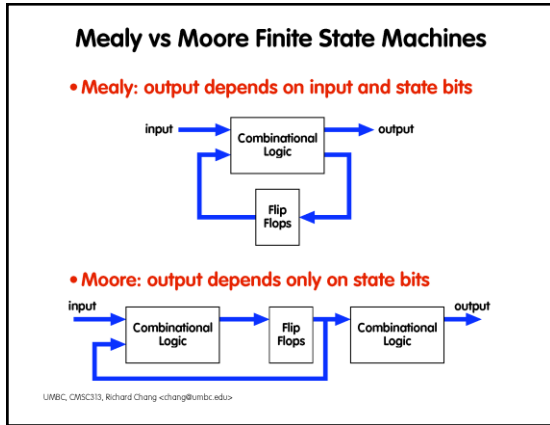
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## EXAMPLE: VENDING MACHINE

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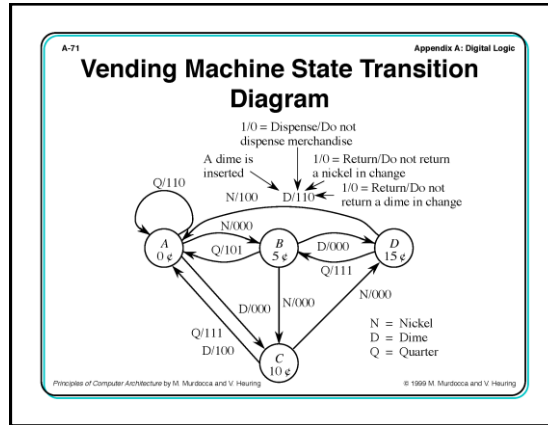
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A-70 Appendix A: Digital Logic

### Example: A Vending Machine Controller

- **Example:** Design a finite state machine for a vending machine controller that accepts nickels (5 cents each), dimes (10 cents each), and quarters (25 cents each). When the value of the money inserted equals or exceeds twenty cents, the machine vends the item and returns change if any, and waits for next transaction.
- Implement with PLA and D flip-flops.

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A-72 Appendix A: Digital Logic

### Vending Machine State Table and State Assignment

Input	N	D	Q
P.S.	00	01	10
A	B/000	C/000	A/110
B	C/000	D/000	A/101
C	D/000	A/100	A/111
D	A/100	A/110	B/111

Input	N	D	Q
P.S.	$x_1x_0$ 00	$x_1x_0$ 01	$x_1x_0$ 10
	$x_1s_0 / z_2z_1z_0$		
A:00	01/000	10/000	00/110
B:01	10/000	11/000	00/101
C:10	11/000	00/100	00/111
D:11	00/100	00/110	01/111

(a) (b)

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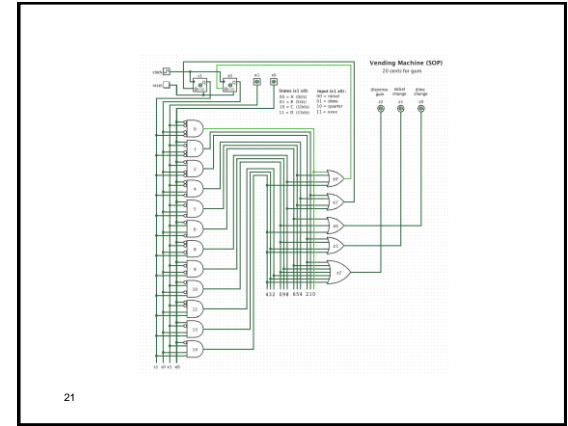
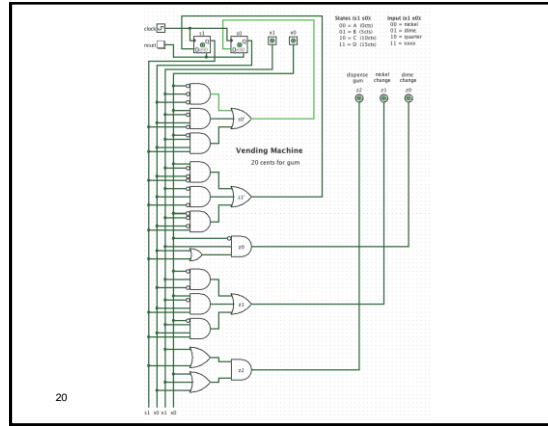
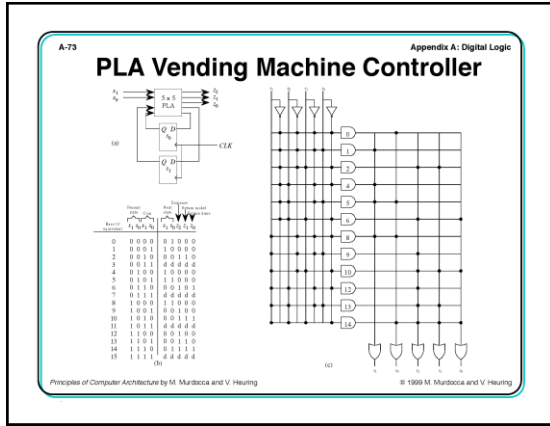
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